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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/443,160	11/19/1999	DAVID L. ISAMAN	130.1012.02	6854

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STMICROELECTRONICS, INC.  
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EXAMINER

ART UNIT PAPER NUMBER

DATE MAILED: 09/18/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Notification of Non-Compliant Appeal Brief (37 CFR 41.37)</b>	<b>Application No.</b> 09/443,160	<b>Applicant(s)</b> ISAMAN, DAVID L.	
	<b>Examiner</b> Daniel Pan	<b>Art Unit</b> 2183	

**--The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

The Appeal Brief filed on 30 June 2006 is defective for failure to comply with one or more provisions of 37 CFR 41.37.

To avoid dismissal of the appeal, applicant must file an amended brief or other appropriate correction (see MPEP 1205.03) within **ONE MONTH or THIRTY DAYS** from the mailing date of this Notification, whichever is longer.

**EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136.**

1. ☐ The brief does not contain the items required under 37 CFR 41.37(c), or the items are not under the proper heading or in the proper order.
2. ☐ The brief does not contain a statement of the status of all claims, (e.g., rejected, allowed, withdrawn, objected to, canceled), or does not identify the appealed claims (37 CFR 41.37(c)(1)(iii)).
3. ☐ At least one amendment has been filed subsequent to the final rejection, and the brief does not contain a statement of the status of each such amendment (37 CFR 41.37(c)(1)(iv)).
4. ☒ (a) The brief does not contain a concise explanation of the subject matter defined in each of the independent claims involved in the appeal, referring to the specification by page and line number and to the drawings, if any, by reference characters; and/or (b) the brief fails to: (1) identify, for each independent claim involved in the appeal and for each dependent claim argued separately, every means plus function and step plus function under 35 U.S.C. 112, sixth paragraph, and/or (2) set forth the structure, material, or acts described in the specification as corresponding to each claimed function with reference to the specification by page and line number, and to the drawings, if any, by reference characters (37 CFR 41.37(c)(1)(v)).
5. ☐ The brief does not contain a concise statement of each ground of rejection presented for review (37 CFR 41.37(c)(1)(vi)).
6. ☐ The brief does not present an argument under a separate heading for each ground of rejection on appeal (37 CFR 41.37(c)(1)(vii)).
7. ☐ The brief does not contain a correct copy of the appealed claims as an appendix thereto (37 CFR 41.37(c)(1)(viii)).
8. ☐ The brief does not contain copies of the evidence submitted under 37 CFR 1.130, 1.131, or 1.132 or of any other evidence entered by the examiner **and relied upon by appellant in the appeal**, along with a statement setting forth where in the record that evidence was entered by the examiner, as an appendix thereto (37 CFR 41.37(c)(1)(ix)).
9. ☐ The brief does not contain copies of the decisions rendered by a court or the Board in the proceeding identified in the Related Appeals and Interferences section of the brief as an appendix thereto (37 CFR 41.37(c)(1)(x)).
10. ☒ Other (including any explanation in support of the above items):

See Continuation Sheet.

Continuation of 10. Other (including any explanation in support of the above items): This is a continuation of the item number 4 above. A concise explanation of the subject matter defined in claim 2 is not included in the Brief. Appellant does include page and line numbers. However, no concise explanation of the subject matter referring the specification can be found in the Brief. For example, appellant states that the instruction 151 is detected without requiring computation of an external memory address of the first memory location for the instruction by citing page 9, lines 9-11 (see Brief page 3, lines 5-7). However, this is a conclusive statement, not an explanation. Appellant is advised to provide a concise explanation of the subject matter defined in each claim by referring to page number and line number in the next response. Furthermore, the cited portions by appellant, page 8, line 6-page 9, line 7, page 11, line 8-page 12, line 11, page 9, lines 9-11, do not referring to the subject defined in claim 2. Claim 2 recites the instruction is detected without requiring computation of an external memory address of the first memory location for the instruction (see claim 2, lines 2-4). However, page 8, line 6 -page 9, line 7 and page 11, line 8-page 12, line 11 recite determination of instructions that load data from external memory or store data from memory. No "instruction is detected without requiring computation" can be found in the cited portions. In fact cited Page 8, lines 10-11 teaches "computes the effective reference address of the instruction 151." Therefore, the cited portions are confusing. As to the cited page 9, lines 9-11, although the cited portion teaches "When the bypass signal is generating, the address computation stage 130 does not have to compute the actual effective address for the microprocessor 100 to act on the knowledge that the instructions 151 refer to identical locations in external memory", this is not the same as "instruction is detected without requiring computation of an external memory address of the first memory location for the instruction" as set forth in claim 2. The subject matter defined in claim 2 is not found in the cited portions. Cited portion of page 9, lines 9-11 does not teach an instruction is detected without requiring computation of external memory address. Instead, Page 9, lines 9-11 teaches when the bypass signal is generating, the address computation does not have to compute the actual effective address. Applicant is suggested to point out the specification by page and line numbers the subject matter defined in claim 2 in the next response.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dan Pan whose telephone number is 571 272 4172. The examiner can normally be reached on M-F from 8:30 AM to 4:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chan, can be reached on 571 272 4162. The fax phone number for the organization where this application or proceeding is assigned is 703 306 5404.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

**21 Century Strategic Plan**

DANIEL H. PAN  
PRIMARY EXAMINER  
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